

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al.
Patent No. : 7,546,444
Issue Date : June 9, 2009
Serial No. : 10/070,091
Filed : June 28, 2002

Art Unit : 2183
Examiner : Eric Coleman
Conf. No. : 7309

Title : REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR
ARCHITECTURE

MAILSTOP CERTIFICATE OF CORRECTIONS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF REQUEST FOR CERTIFICATE OF CORRECTION

Applicants hereby request that a certificate of correction be issued for the above patent in accordance with the attached request.

All errors sought to be corrected were made in printing by the Patent and Trademark Office, and no fee is believed to be due. Please apply any other charges or credits to deposit account 06-1050, referencing the attorney docket number shown above.

Respectfully submitted,

Date: July 8, 2009

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Here
Only**UNITED STATES PATENT AND TRADEMARK OFFICE**
CERTIFICATE OF CORRECTIONPage 1 of 1

PATENT NO. :: 7,546,444

APPLICATION NO :: 10/070,091

DATED :: JUNE 9, 2009

INVENTOR(S) :: GILBERT WOLRICH, MATTHEW J. ADILETTA, WILLIAM R. WHEELER, DEBRA
BERNSTEIN AND DONALD F. HOOPER

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 4:

column 9, line 24:
add --to-- after "access"

Claim 6:

column 9, line 29:
delete "allow", and replace with --allows--

Claim 17:

column 10, line 28:
delete "allow", and replace with --allows--

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